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(71) Applicant (for all designated States except US): KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): VAN BERKEL, Cornelis, H. [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). NAS, Ricky, J., M. [NL/NL]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(74) Agent: DULJVESTIJN, Adrianus, J.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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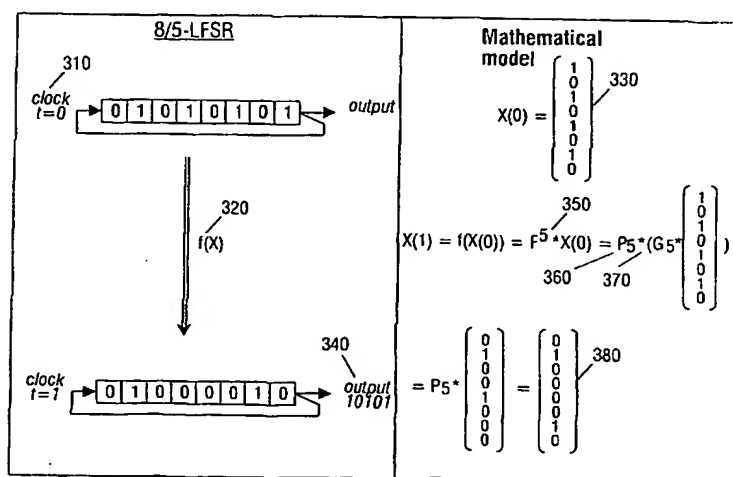
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[Continued on next page]

(54) Title: CONFIGURABLE MULTI-STEP LINEAR FEEDBACK SHIFT REGISTER



(57) Abstract: The state transition of a linear feedback shift register (LFSR) controlled by a clock (310) with length N and step size W, W being at least two, is accomplished via a next-state function (320). The next-state function deploys a state transition matrix (350). The state vector (330), which represents the contents of the LFSR, is either multiplied sequentially by the state transition matrix or multiplied by the state transition matrix to the power of W (multiple state transition matrix). The method and the LFSR according to the invention are characterized in that the multiple state transition matrix is decomposed in a first matrix (360) and a second matrix (370), the first matrix comprising at most $N + W + 1$ different expressions and the second matrix comprising at most $N + W + 1$ different expressions. The LFSR further comprises means to multiply the state vector by the second matrix and the first matrix, and means for computing the first matrix. The invention overcomes the shortcomings of configurable multi-step linear feedback shift registers because the amount of time needed to generate the output can be reduced significantly.



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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	<p>US 5 412 665 A (PATEL PIYUSHKUMAR C ET AL) 2 May 1995 (1995-05-02) cited in the application abstract column 3, line 63 - column 4, line 67 figure 2</p> <p style="text-align: center;">----- -/--</p>	<p>1-4</p> <p>5-8</p>

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Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel (+31-70) 340-2040, Tx. 31 651 epo nl,
Fax (+31-70) 340-3016

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C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>WANG L-T ET AL INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS: "A HYBRID DESIGN OF MAXIMUM-LENGTH SEQUENCE GENERATORS" PROCEEDINGS OF THE ANNUAL EXPERT SYSTEMS IN GOVERNMENT CONFERENCE. WASHINGTON, 19 - 23 OCTOBER, 1987, WASHINGTON, IEEE COMP. SOC. PRESS, US, vol. CONF. 3, 8 September 1986 (1986-09-08), pages 38-47, XP000746762 abstract page 38, left-hand column, line 16 - right-hand column, line 10 page 40, right-hand column, line 11 - page 41, left-hand column, line 21</p> <p>-----</p>	1-8

INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
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